EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1661	(logic and combinatorial and inverter and buffer)".clm"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/15 20:14
S1	953946	"324"/$.ccls. and logic adj test or circuit and gate and inverter or buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 12:40
S2	5324	"324"/$.ccls. and ((logic adj test) or circuit and gate and (inverter or buffer))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 12:41
S3	671	S2 and exclusive	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 12:42
S4	35	S3 and (combinatorial or combinational)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 12:44
S5	70	S3 and bypass	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 12:45
S6	178	S3 and multiplexer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 12:45
S7	178	S3 and multiplexer$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 13:29
S8	88	S3 and multiplexer$1 and pattern$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/07 13:29
S9	5451	"324"/$.ccls. and ((logic adj test) or circuit and gate and (inverter or buffer))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/15 19:47

EAST Search History

S10	686	S9 and exclusive	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/15 19:47
S11	180	S10 and multiplexer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/15 19:47
S12	73	S10 and bypass	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/15 19:47
S13	36	S10 and (combinatorial or combinational)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/15 19:47
S14	89	S10 and multiplexer$1 and pattern$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/15 20:14
S15	180	S10 and multiplexer$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/15 19:47
S16	990123	"324"/$.ccls. and logic adj test or circuit and gate and inverter or buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/15 19:47

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	(combinatorial and node$1 and buffer and inverter and logica$3 and test).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/10/15 20:01
L2	1	(combinatorial and node$1 and buffer and inverter and logica$3 and test).clm.	US-PGPUB; USPAT	ADJ	ON	2006/10/15 20:01
L4	1	(combin$4 and node$1 and buffer and inverter and logica$3 and test).clm.	US-PGPUB; USPAT	ADJ	ON	2006/10/15 20:01
L5	4	(combina$4 and node$1 and buffer and inverter and logica$3 and test).clm.	US-PGPUB; USPAT	ADJ	ON	2006/10/15 20:02
L6	4	(connection and input and output and node$1 and buffer and inverter and logica$3 and test).clm.	US-PGPUB; USPAT	ADJ	ON	2006/10/15 20:04
L8	9	(connection and input and output and node$1 and buffer and inverter and data and logica$3).clm.	US-PGPUB; USPAT	ADJ	ON	2006/10/15 20:04
L9	4	(connection and input and output and coupl$2 and node$1 and buffer and inverter and data and logica$3).clm.	US-PGPUB; USPAT	ADJ	ON	2006/10/15 20:05
L10	81	(connection and input and output and coupl$2 and node$1 and data and logica$3).clm.	US-PGPUB; USPAT	ADJ	ON	2006/10/15 20:05
L12	1	(connection and combinatorial and coupl$2 and node$1 and data and inverter and buffer and logica$3).clm.	US-PGPUB; USPAT	ADJ	ON	2006/10/15 20:05
L13	2	(combinatorial and coupl$2 and node$1 and data and inverter and buffer and logica$3).clm.	US-PGPUB; USPAT	ADJ	ON	2006/10/15 20:06
L14	3	(combinatorial and coupl$2 and node$1 and inverter and buffer and logica$3).clm.	US-PGPUB; USPAT	ADJ	ON	2006/10/15 20:06

Interference Search